

Notice of Allowability

Application No.

10/706,778

Examiner

ANH PHUNG

Applicant(s)

CHO, TAEHEE

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the Election received on Feb. 7, 2005.
2. ☒ The allowed claim(s) is/are 1-6 and 13-24.
3. ☐ The drawings filed on _____ are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.


Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☒ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☒ to Paper No./Mail Date 3/3/05.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 12/8/04
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other Search History.


ANH PHUNG
PRIMARY EXAMINER

OFFICE ACTION

Examiner's amendment

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CAR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

In the claims:

Cancel non-elected claims **7-12** due to the Election without traverse on February 7, 2005.

The examiner's amendment has been made in order to place the application in a condition for allowance.

2. In response to the Election received on February 7, 2005, the elected claims **1-6** and **13-24** are pending in the application and non-elected claims **7-12** are cancelled per Examiner's amendment above.

Priority

3. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

4. This office acknowledges receipt of the following items from the Applicant:

Information Disclosure Statement (IDS) filed on December 8, 2004. Information disclosed and listed on PTO 1449 was considered.

Drawings

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5. New corrected drawings in compliance with 37 CFR 1.121(d) are required in this application because Figures 1-9 has been corrected with "Background Art". Applicant is advised to employ the services of a competent patent draftsman outside the Office, as the U.S. Patent and Trademark Office no longer prepares new drawings. The corrected drawings are required in reply to the Office action to avoid abandonment of the application. The requirement for corrected drawings will not be held in abeyance.

Examiner's Statement of Reasons for Allowance

6. Claims **1-6** and **13-24** are allowed.

Miyazaki et al. (US 6,320,799), Yokozeki (US 6,243,319), Takita et al. (US 6,198,686) and Kim et al. (US 5,912,857) disclose a row decoder similar to that of the present application, but fail to teach:

"a first NAND gate for generating a reset gate signal in response to an address signal and a reset signal; a second NAND gate for generating a selection gate signal in response to the address signal and a selection signal; a first latch for resetting an output thereof in response to an address latch signal and latching the address signal as the output in response to the address latch signal and the address signal; a third NAND gate for receiving the address signal and a transmitted signal; a fourth NAND gate for receiving the output of the latch and a shutter transmitted signal; and an OR gate for receiving the outputs of the third and fourth NAND gates and generating a transmitted gate signal" as claimed in the independent claim 1; or

"a first NAND gate for generating a reset gate signal in response to the address signal and a reset signal; a second NAND gate for generating a selection gate signal in

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response to the address signal and a selection signal; a first latch for resetting an output thereof in response to an address latch signal and latching the address signal as the output in response to the address latch signal and the address signal; a third NAND gate for receiving the address signal and a transmitted signal; a fourth NAND gate for receiving the output of the latch and a shutter transmitted signal; and an OR gate for receiving the outputs of the third and fourth NAND gates and generating a transmitted gate signal” as claimed in the independent claim **13**; or

“gate reset means for generating a reset gate signal in response to an address signal and a reset signal; gate selection means for generating a selection gate signal in response to the address signal and a selection signal; first latch reset means for resetting a latch output signal in response to an address latch signal; first latch set means for latching the address signal as the latch output signal in response to the address latch signal and the address signal; and gate transmission means for generating a transmitted gate signal in response to the address signal, a transmitted signal, the latched output signal, and a shutter transmitted signal” as claimed in the independent claim **19**.

Any comments considered necessary by applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comment on Statement of Reasons for Allowance”.

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Conclusion

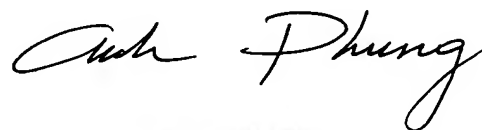
7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **ANH PHUNG** whose telephone number is **(571) 272-1883**. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **RICHARD ELMS**, can be reached on **(571) 272-1869**. The fax phone number for the organization where this application or proceeding is assigned is **(703) 872-9306**.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at **866-217-9197** (toll-free).

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AKP



**ANH PHUNG
PRIMARY EXAMINER**